

Cadence Design Contest 2018

This certifies that

Karthik Sridhar R K and Dayalan S

representing

Dr. Mahalingam College of Engineering and Technology

under the guidance of

Ms. S Kalaiselvi

were **Finalists** in the Undergraduate category

for the paper titled

ASIC Implementation of Truncation Based Multiplier

Madhavi Rao
Marketing Director
Cadence Design Systems

 $z_{4} = z_{4}$ or $z_{2} = -b_{+} \sqrt{1}$ $\int_{a_{2}}^{a_{2}} f(x) = a \left[(x_{+} \frac{b}{2a})^{2} - a \right] \left[(x_{+} \frac{b}{2a})^{2} - a \right]$